UNITED STATES DEPARTMENT OF COMMERCE
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/056,656	04/07/98	PRIEM	C NV30

022903

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WM02/0328

EXAMINER
CHAUHAN, U

ART UNIT	PAPER NUMBER
2671	18

DATE MAILED:

03/28/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.

EXAMINER

ART UNIT	PAPER

18

DATE MAILED:

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Ulka J. Chauhan
Primary Examiner
Art Unit: 2671

	Application No.	Applicant(s)
Advisory Action	09/056,656	PRIEM ET AL.
	Examiner	Art Unit	
	Ulka J. Chauhan	2671	

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 12 March 2001 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE. Therefore, further action by the applicant is required to avoid abandonment of this application. A proper reply to a final rejection under 37 CFR 1.113 may only be either: (1) a timely filed amendment which places the application in condition for allowance; (2) a timely filed Notice of Appeal (with appeal fee); or (3) a timely filed Request for Continued Examination (RCE) in compliance with 37 CFR 1.114.

<u>PERIOD FOR REPLY</u> [check only a) or b)]

a) ☒ The period for reply expires <u>3</u> months from the mailing date of the final rejection.

b) ☐ In view of the early submission of the proposed reply (within two months as set forth in MPEP § 706.07 (f)), the period for reply expires on the mailing date of this Advisory Action, OR continues to run from the mailing date of the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

1. ☐ A Notice of Appeal was filed on _____. Appellant's Brief must be filed within the period set forth in 37 CFR 1.192(a), or any extension thereof (37CFR 1.191(d)), to avoid dismissal of the appeal.

2. ☒ The proposed amendment(s) will be entered upon the timely submission of a Notice of Appeal and Appeal Brief with requisite fees.

3. ☐ The proposed amendment(s) will not be entered because:

    (a) ☐ they raise new issues that would require further consideration and/or search. (see NOTE below);

    (b) ☐ they raise the issue of new matter. (see Note below);

    (c) ☐ they are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or

    (d) ☐ they present additional claims without canceling a corresponding number of finally rejected claims.
        NOTE: _____.

4. ☒ Applicant's reply has overcome the following rejection(s): <u>*rejection of claims 42-61 under 35 USC 11, first paragraph.*</u>

5. ☐ Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).

6. ☒ The a)☐ affidavit, b)☐ exhibit, or c)☒ request for reconsideration has been considered but does NOT place the application in condition for allowance because: *See Continuation Sheet.*

7. ☐ The affidavit or exhibit will NOT be considered because it is not directed SOLELY to issues which were newly raised by the Examiner in the final rejection.

8. ☒ For purposes of Appeal, the status of the claim(s) is as follows (see attached written explanation, if any):

    Claim(s) allowed: _____.

    Claim(s) objected to: _____.

    Claim(s) rejected: <u>*42-69.*</u>

    Claim(s) withdrawn from consideration: _____.

9. ☐ The proposed drawing correction filed on _____ a)☐ has b)☐ has not been approved by the Examiner.

10. ☐ Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s). _____.

11. ☐ Other:

Ulka J. Chauhan
Primary Examiner
Art Unit: 2671

U.S. Patent and Trademark Office

PTO-303 (Rev. 01-01)   Advisory Action   Part of Paper No. 18

Continuation of 6. does NOT place the application in condition for allowance because: the Applicant argues that Gannett does not teach "graphics accelerator including ... a replacement contro component that implements a replacement policy". In reviewing the specification supporting this limitation, it was found that the specification on pages 19-21 discloses the replacement policy of the invention. However no clear disclosure was found that described the replacement control component included in the graphics accelerator. In reference to the replacement policy, the disclosure describes that steps taken by the invention to implement the replacement but does not describe a specific component comprised in the accelerator. Applicants cites pages 9 and 10 to indicate that texels are accessed and provided to the cache utilizing the DMA engine. However this citation does not disclose that the DMA engine implements the replacement policy. Gannett teaches that the texture mapping chip includes sets of registers that support the software in determining blocks for replacement at col. 42 lines 16-21. As such, Gannett's TEXTURE INTERRUPT MANAGING DAEMON 160 which is a process running on the processor of the host computer does interact with the graphics hardware driver and device to manage the storing of the texture data within the local memory of the hardware device. Therefore, the hardware device is understood to include components that participate in the replacement policy. And as such, the graphics hardware is understood to include these components. Applicant also argues that Gannett does not discose forming an address by interleaving individual bit values of the two coordinates. However as noted in the final rejection, Gannett teaches that texels within the cache are addressed using S and T bits at col. 23 lines 26-38. Therefore Gannett teaches combining the values of the two coordinates, S and T, to form a texel address .

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